

COMMITTEE MEMBER



1. Chip Design Reviewer, Taiwan Semiconductor Research Institute, TSRI (國家實驗研究院台灣半導體研究中心), 2008-2024.
2. Small Business Innovation Research (SBIR) project, member of review committee in electronic field, Department of Industrial Technology (DoIT), Ministry of Economic Affairs, 2009-2023.
3. Golden Silicon Awards (旺宏金矽獎), member of review committee, 2021-2023
4. Industry Chair of the 31st IEEE Asia Test Symposium (ATS), Nov. 2022.
5. A+ Business Innovation, Research and Development project, member of review committee, Department of Industrial Technology (DoIT), Ministry of Economic Affairs, 2014-2021.
6. Session Chair of the International Symposium on VLSI-DAT, Apr. 2018.
7. Program Committee Member and Session Chair of the Asia Test Symposium (ATS), Nov. 2017.
8. Program Committee Member of the IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Oct. 2017.
9. Technical Program Committee Member of the VLSI Test Technology Workshop, July, 2017.
10. Program Committee Member of the IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Oct. 2016.
11. Session Chair of Taiwan Tech. & Kyutech Workshop on VLSI Test Technologies, Sep. 2016.
12. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2016.
13. Technical Program Committee Member of the VLSI Test Technology Workshop, July, 2016.
14. App contest, member of review committee, Synopsys, 2013-2015.
15. Intelligent electronics innovation and design contest, member of review committee, Ministry of Education, 2013-2015.
16. The series courses of interdisciplinary intelligent electronics, member of review committee, Ministry of Education, 2011-2015.
17. Program Committee Member of the IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Oct. 2015.
18. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2015.
19. Technical Program Committee Member of the VLSI Test Technology Workshop, July, 2015.
20. Program Committee Member of the IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Oct. 2014.
21. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2014.

22. Technical Program Committee Member of the VLSI Test Technology Workshop, July, 2014.
23. Program Committee Member and Session Chair of the Asia Test Symposium (ATS), Nov. 2013.
24. Program Committee Member of the IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Sep. 2013.
25. Session Chair of Taiwan Tech. & Kyutech Workshop on VLSI Test Technologies, Sep. 2013.
26. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2013.
27. Panel Discussion Chair of the VLSI Test Technology Workshop, July, 2013.
28. Program Committee Member of the IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Sep. 2012.
29. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2012.
30. Technical Program Committee Member of the VLSI Test Technology Workshop, July, 2012.
31. Local Arrangement of the International Mixed-Signals, Sensors, and Systems Test Workshop, May, 2012.
32. Distinguished research professor, Information and Communication Laboratory (ICL), Industrial Technology Research Institute (ITRI), 2011.
33. Program Committee Member of the 2nd IEEE International Workshop on Testing --- 3D Stacked Integrated Circuits, Sep. 2011.
34. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2011.
35. Technical Program Committee Member of the VLSI Test Technology Workshop, July, 2011.
36. Technical Program Committee Member of the VLSI Design/CAD Symposium, Aug., 2010.
37. Local Arrangement of the VLSI Test Technology Workshop, July, 2010.
38. Technical Program Committee Member of the 20th VLSI Design/CAD Symposium, Aug., 2009.
39. Session Chair of the VLSI Test Technology Workshop, July, 2009.
40. Referee of IEEE TC, TCAD, TVLSI, TCAS, TBCAS, TCSVT, TIAM, Microelectronics Journal, IET Circuits, Devices and System.

MEDIA COVERAGE

1. [youtube.com](https://www.youtube.com) 數位系統設計, YouTube, 2023
2. [ctimes.com.tw](https://www.ctimes.com.tw) 小晶片 Chiplet 在夯什麼? CTIMES, 2021

