

These resistors set clock generators to sensible values when the FPGA is not configured. CLK1 and CLK2 are set to 4.8 MHz

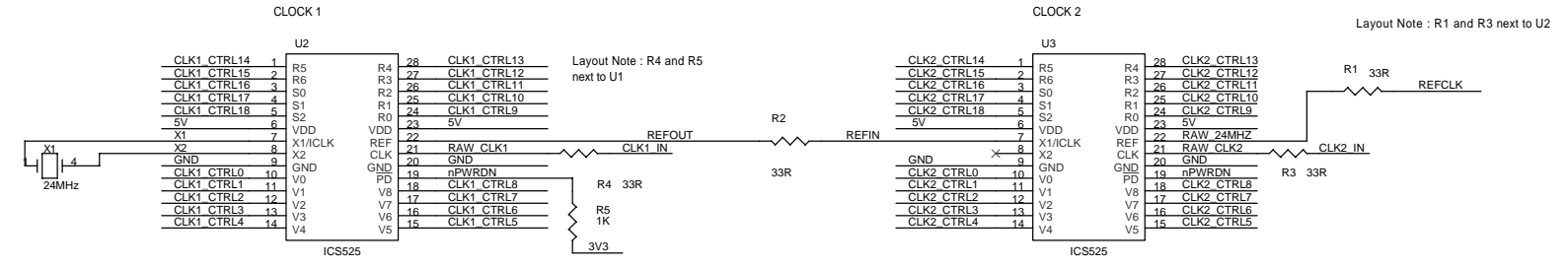
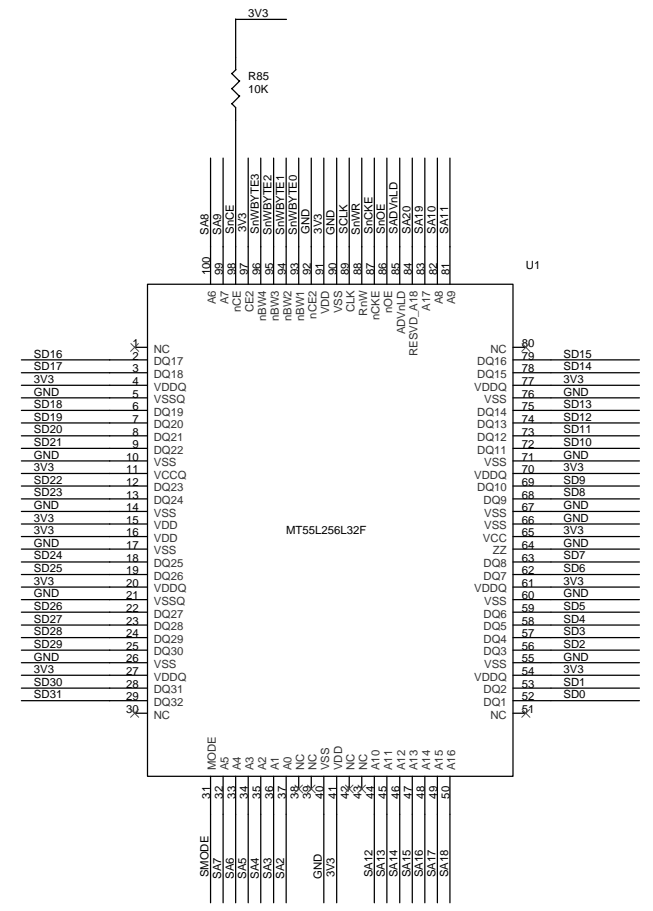
$X1 = 24 \text{ MHz}$

Clock frequency = $2 \times X1 \frac{(\text{CLK_CTRL}[8..0] + 8)}{S * (\text{CLK_CTRL}[15..9] + 2)}$

The following operating ranges should be observed:

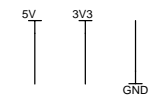
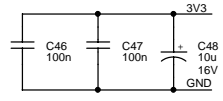
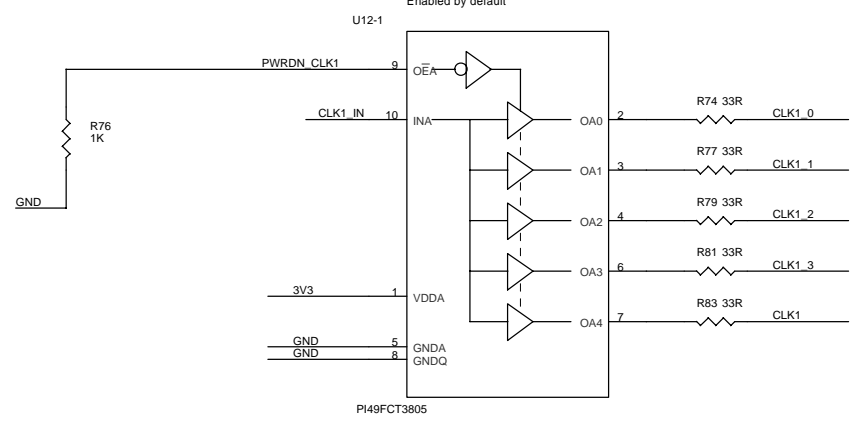
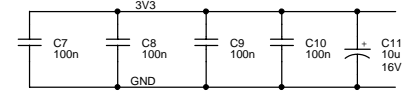
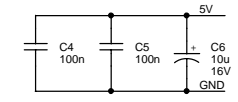
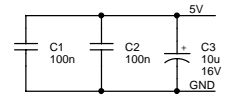
$$10 \text{ MHz} < 2 \times X1 \frac{(\text{CLK_CTRL}[8..0] + 8)}{(\text{CLK_CTRL}[15..9] + 2)} < 200 \text{ kHz} < \frac{X1}{(\text{CLK_CTRL}[15..9] + 2)}$$

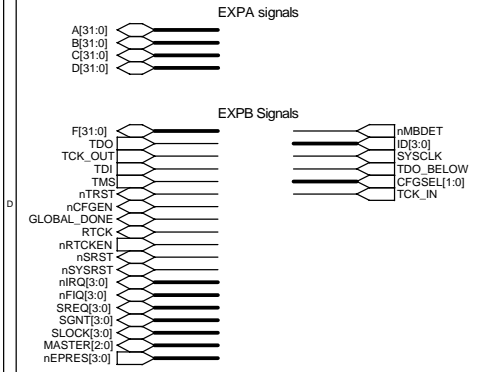
S=10 when CLK_CTRL[18..16]= "000"
 S=2 when CLK_CTRL[18..16]= "001"
 S=8 when CLK_CTRL[18..16]= "010"
 S=4 when CLK_CTRL[18..16]= "011"
 S=5 when CLK_CTRL[18..16]= "100"
 S=7 when CLK_CTRL[18..16]= "101"
 S=9 when CLK_CTRL[18..16]= "110"
 S=6 when CLK_CTRL[18..16]= "111"



Nets RAW_CLK1 and CLK1_IN are up to a 160MHz clock signal, this track should be as short as possible.

Nets RAW_CLK2 and CLK2_IN are up to a 160MHz clock signal, this track should be as short as possible.





EXPB socket

A0	1	101	GND
GND	2	102	D0
A1	3	103	D1
A2	4	104	D2
A3	5	105	GND
GND	6	106	D3
A4	7	107	D4
A5	8	108	D5
A6	9	109	GND
GND	10	110	D6
A7	11	111	D7
A8	12	112	D8
A9	13	113	GND
GND	14	114	D9
A10	15	115	D10
A11	16	116	D11
A12	17	117	GND
GND	18	118	D12
A13	19	119	D13
A14	20	120	D14
A15	21	121	GND
GND	22	122	D15
A16	23	123	D16
A17	24	124	D17
A18	25	125	GND
GND	26	126	D18
A19	27	127	D19
A20	28	128	D20
A21	29	129	GND
GND	30	130	D21
A22	31	131	D22
A23	32	132	D23
A24	33	133	GND
GND	34	134	D24
A25	35	135	D25
A26	36	136	D26
A27	37	137	GND
GND	38	138	D27
A28	39	139	D28
A29	40	140	D29
A30	41	141	GND
GND	42	142	D30
A31	43	143	D31
B0	44	144	C0
B1	45	145	GND
GND	46	146	C1
B2	47	147	C2
B3	48	148	C3
B4	49	149	GND
GND	50	150	C4
B5	51	151	C5
B6	52	152	C6
B7	53	153	GND
GND	54	154	C7
B8	55	155	C8
B9	56	156	C9
B10	57	157	GND
GND	58	158	C10
B11	59	159	C11
B12	60	160	C12
B13	61	161	GND
GND	62	162	C13
B14	63	163	C14
B15	64	164	C15
B16	65	165	GND
GND	66	166	C16
B17	67	167	C17
B18	68	168	C18
B19	69	169	GND
GND	70	170	C19
B20	71	171	C20
B21	72	172	C21
B22	73	173	GND
GND	74	174	C22
B23	75	175	C23
B24	76	176	C24
B25	77	177	GND
GND	78	178	C25
B26	79	179	C26
B27	80	180	C27
B28	81	181	GND
GND	82	182	C28
B29	83	183	C29
B30	84	184	C30
B31	85	185	GND
GND	86	186	C31
5V	87	187	3V3
3V3	88	188	12V
5V	89	189	3V3
3V3	90	190	12V
5V	91	191	3V3
3V3	92	192	12V
5V	93	193	3V3
3V3	94	194	12V
5V	95	195	3V3
3V3	96	196	12V
5V	97	197	3V3
3V3	98	198	12V
5V	99	199	3V3
3V3	100	200	12V

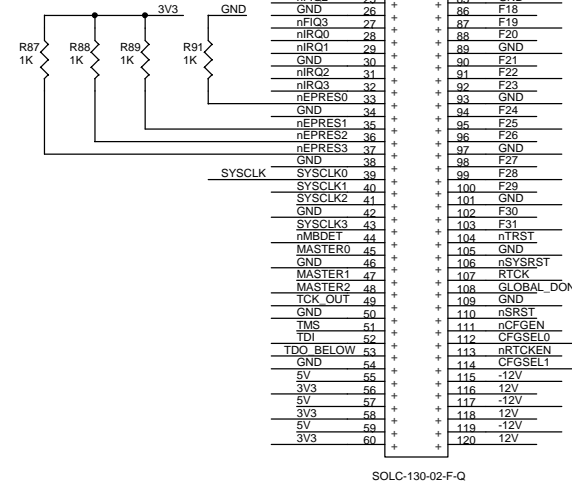
Print on silk screen:
 '20' by pin20, '40' by pin40, '60' by pin 60,
 '80' by pin80, '100' by pin100, '120' by pin120,
 '140' by pin140, '160' by pin160, '180' by pin180
 & '200' by pin200

Also print 'EXPA' near to the connector.

UNDERNEATH

EXPB socket

SREQ0	1	61	GND
GND	2	62	F0
SREQ1	3	63	F1
SREQ2	4	64	F2
SREQ3	5	65	GND
GND	6	66	F3
SGNT0	7	67	F4
SGNT1	8	68	F5
SGNT2	9	69	GND
GND	10	70	F6
SGNT3	11	71	F7
SLOCK0	12	72	F8
SLOCK1	13	73	GND
GND	14	74	F9
SLOCK2	15	75	F10
SLOCK3	16	76	F11
ID0	17	77	GND
GND	18	78	F12
ID1	19	79	F13
ID2	20	80	F14
ID3	21	81	GND
GND	22	82	F15
nFIQ0	23	83	F16
nFIQ1	24	84	F17
nFIQ2	25	85	GND
GND	26	86	F18
nFIQ3	27	87	F19
nRO	28	88	F20
nIRO1	29	89	GND
GND	30	90	F21
nIRO2	31	91	F22
nIRO3	32	92	F23
nEPRES0	33	93	GND
GND	34	94	F24
nEPRES1	35	95	F25
nEPRES2	36	96	F26
nEPRES3	37	97	GND
GND	38	98	F27
SYSCLK0	39	99	F28
SYSCLK1	40	100	F29
SYSCLK2	41	101	GND
GND	42	102	F30
SYSCLK3	43	103	F31
nMBDET	44	104	nTRST
MASTER0	45	105	GND
GND	46	106	nSYSRST
MASTER1	47	107	RTCK
MASTER2	48	108	GLOBAL_DONE
TCK_OUT	49	109	GND
GND	50	110	nSRST
TMS	51	111	nCFGEN
TDI	52	112	CFGSEL0
TDO_BELOW	53	113	nRTCKEN
GND	54	114	CFGSEL1
5V	55	115	-12V
3V3	56	116	12V
5V	57	117	-12V
3V3	58	118	12V
5V	59	119	-12V
3V3	60	120	12V



Print on silk screen:
 '20' by pin20, '40' by pin40, '60' by pin 60,
 '80' by pin80, '100' by pin100, '120' by pin120,

Also print 'EXPB' near to the connector.

SOLC-130-02-F-Q

EXPB plug

EXPB plug

A0	1	101	GND
GND	2	102	D0
A1	3	103	D1
A2	4	104	D2
A3	5	105	GND
GND	6	106	D3
A4	7	107	D4
A5	8	108	D5
A6	9	109	GND
GND	10	110	D6
A7	11	111	D7
A8	12	112	D8
A9	13	113	GND
GND	14	114	D9
A10	15	115	D10
A11	16	116	D11
A12	17	117	GND
GND	18	118	D12
A13	19	119	D13
A14	20	120	D14
A15	21	121	GND
GND	22	122	D15
A16	23	123	D16
A17	24	124	D17
A18	25	125	GND
GND	26	126	D18
A19	27	127	D19
A20	28	128	D20
A21	29	129	GND
GND	30	130	D21
A22	31	131	D22
A23	32	132	D23
A24	33	133	GND
GND	34	134	D24
A25	35	135	D25
A26	36	136	D26
A27	37	137	GND
GND	38	138	D27
A28	39	139	D28
A29	40	140	D29
A30	41	141	GND
GND	42	142	D30
A31	43	143	D31
B0	44	144	C0
B1	45	145	GND
GND	46	146	C1
B2	47	147	C2
B3	48	148	GND
B4	49	149	C3
GND	50	150	C4
B5	51	151	C5
B6	52	152	C6
B7	53	153	GND
GND	54	154	C7
B8	55	155	C8
B9	56	156	C9
B10	57	157	GND
GND	58	158	C10
B11	59	159	C11
B12	60	160	C12
B13	61	161	GND
GND	62	162	C13
B14	63	163	C14
B15	64	164	C15
B16	65	165	GND
GND	66	166	C16
B17	67	167	C17
B18	68	168	C18
B19	69	169	GND
GND	70	170	C19
B20	71	171	C20
B21	72	172	C21
B22	73	173	GND
GND	74	174	C22
B23	75	175	C23
B24	76	176	C24
B25	77	177	GND
GND	78	178	C25
B26	79	179	C26
B27	80	180	C27
B28	81	181	GND
GND	82	182	C28
B29	83	183	C29
B30	84	184	C30
B31	85	185	GND
GND	86	186	C31
5V	87	187	3V3
3V3	88	188	12V
5V	89	189	3V3
3V3	90	190	12V
5V	91	191	3V3
3V3	92	192	12V
5V	93	193	3V3
3V3	94	194	12V
5V	95	195	3V3
3V3	96	196	12V
5V	97	197	3V3
3V3	98	198	12V
5V	99	199	3V3
3V3	100	200	12V

Print on silk screen:
 '20' by pin20, '40' by pin40, '60' by pin 60,
 '80' by pin80, '100' by pin100, '120' by pin120,
 '140' by pin140, '160' by pin160, '180' by pin180
 & '200' by pin200

Also print 'EXPA' near to the connector.

TOLC-150-32-F-Q

TOP

EXPB plug

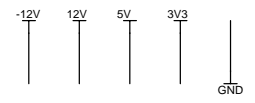
EXPB plug

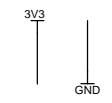
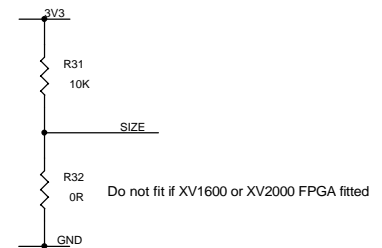
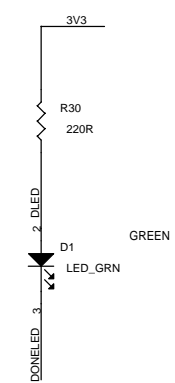
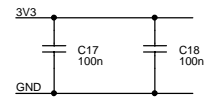
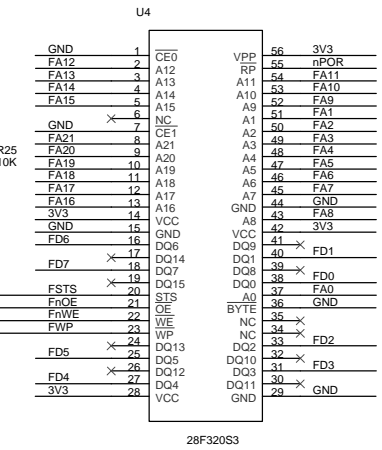
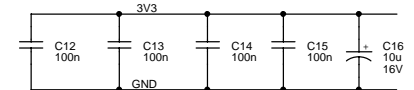
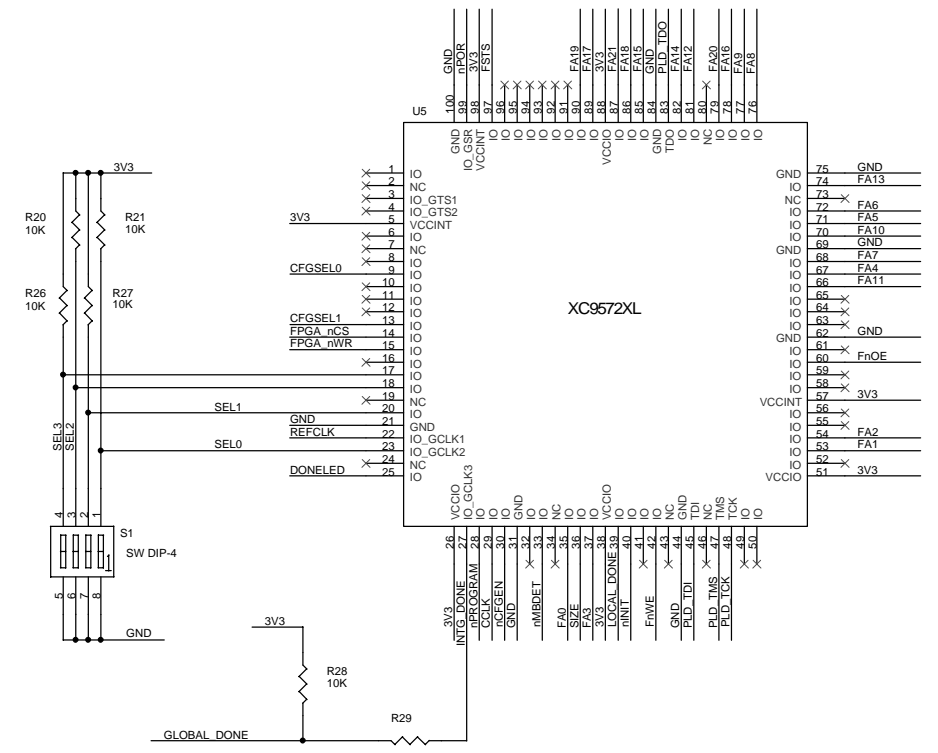
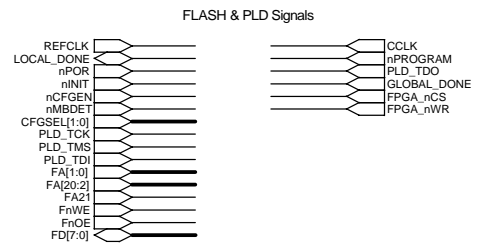
SREQ1	1	61	GND
GND	2	62	F0
SREQ2	3	63	F1
SREQ3	4	64	F2
SREQ0	5	65	GND
GND	6	66	F3
SGNT1	7	67	F4
SGNT2	8	68	F5
SGNT3	9	69	GND
GND	10	70	F6
SGNT0	11	71	F7
SLOCK1	12	72	F8
SLOCK2	13	73	GND
GND	14	74	F9
SLOCK3	15	75	F10
SLOCK0	16	76	F11
ID1	17	77	GND
GND	18	78	F12
ID2	19	79	F13
ID3	20	80	F14
ID0	21	81	GND
GND	22	82	F15
nFIQ1	23	83	F16
nFIQ2	24	84	F17
nFIQ3	25	85	GND
GND	26	86	F18
nFIQ0	27	87	F19
nIRO1	28	88	F20
nIRO2	29	89	GND
GND	30	90	F21
nIRO3	31	91	F22
nIRO0	32	92	F23
nEPRES1	33	93	GND
GND	34	94	F24
nEPRES2	35	95	F25
nEPRES3	36	96	F26
nEPRES0	37	97	GND
GND	38	98	F27
SYSCLK1	39	99	F28
SYSCLK2	40	100	F29
SYSCLK3	41	101	GND
GND	42	102	F30
SYSCLK0	43	103	F31
nMBDET	44	104	nTRST
MASTER0	45	105	GND
GND	46	106	nSYSRST
MASTER1	47	107	RTCK
MASTER2	48	108	GLOBAL_DONE
TCK_IN	49	109	GND
GND	50	110	nSRST
TMS	51	111	nCFGEN
TDI	52	112	CFGSEL0
TDO	53	113	nRTCKEN
GND	54	114	CFGSEL1
5V	55	115	-12V
3V3	56	116	12V
5V	57	117	-12V
3V3	58	118	12V
5V	59	119	-12V
3V3	60	120	12V

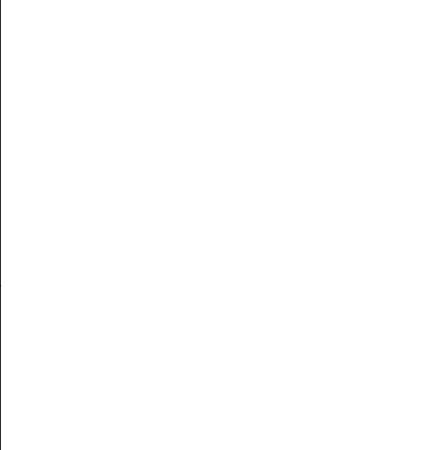
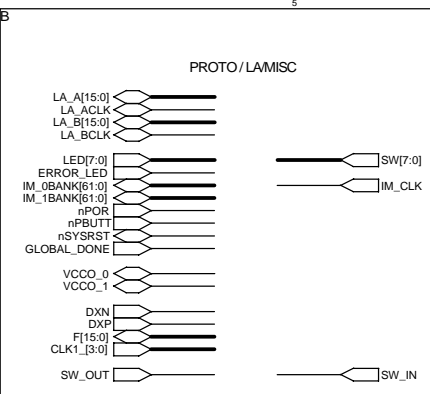
Print on silk screen:
 '20' by pin20, '40' by pin40, '60' by pin 60,
 '80' by pin80, '100' by pin100, '120' by pin120,

Also print 'EXPB' near to the connector.

TOLC-130-32-F-Q





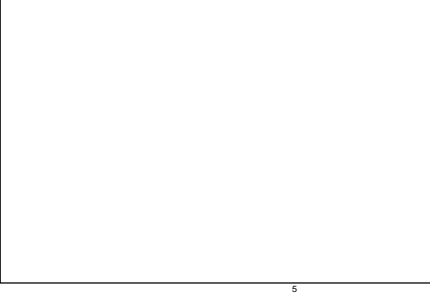
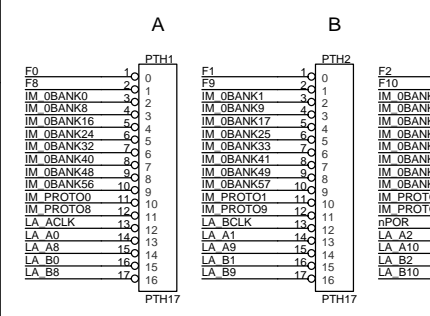


Print on silk screen:
 '20' by pin20, '40' by pin40, '60' by pin 60,
 '80' by pin80, '100' by pin100, '120' by pin120,
 '140' by pin140, '160' by pin160, '180' by pin180
 & '200' by pin200

Also print 'EXPIM' near to the connector.

These are not connectors - they are a grid of plated through holes with a 0.1 inch pitch.

PROTOTYPING AREA



Interface Module socket (bottom)
 Note this is not fitted as standard

1	GND	101	GND
2	GND	102	GND
3	IM_BANK0	103	IM_BANK0
4	IM_BANK1	104	IM_BANK1
5	IM_BANK2	105	IM_BANK2
6	GND	106	GND
7	IM_BANK3	107	IM_BANK3
8	IM_BANK4	108	IM_BANK4
9	IM_BANK5	109	IM_BANK5
10	GND	110	GND
11	IM_BANK6	111	IM_BANK6
12	IM_BANK7	112	IM_BANK7
13	IM_BANK8	113	IM_BANK8
14	GND	114	GND
15	IM_BANK9	115	IM_BANK9
16	IM_BANK10	116	IM_BANK10
17	IM_BANK11	117	IM_BANK11
18	GND	118	GND
19	IM_BANK12	119	IM_BANK12
20	IM_BANK13	120	IM_BANK13
21	IM_BANK14	121	IM_BANK14
22	GND	122	GND
23	IM_BANK15	123	IM_BANK15
24	IM_BANK16	124	IM_BANK16
25	IM_BANK17	125	IM_BANK17
26	GND	126	GND
27	IM_BANK18	127	IM_BANK18
28	IM_BANK19	128	IM_BANK19
29	IM_BANK20	129	IM_BANK20
30	GND	130	GND
31	IM_BANK21	131	IM_BANK21
32	IM_BANK22	132	IM_BANK22
33	IM_BANK23	133	IM_BANK23
34	GND	134	GND
35	IM_BANK24	135	IM_BANK24
36	IM_BANK25	136	IM_BANK25
37	IM_BANK26	137	IM_BANK26
38	GND	138	GND
39	IM_BANK27	139	IM_BANK27
40	IM_BANK28	140	IM_BANK28
41	IM_BANK29	141	IM_BANK29
42	GND	142	GND
43	IM_BANK30	143	IM_BANK30
44	IM_BANK31	144	IM_BANK31
45	IM_BANK32	145	IM_BANK32
46	GND	146	GND
47	IM_BANK33	147	IM_BANK33
48	IM_BANK34	148	IM_BANK34
49	IM_BANK35	149	IM_BANK35
50	GND	150	GND
51	IM_BANK36	151	IM_BANK36
52	IM_BANK37	152	IM_BANK37
53	IM_BANK38	153	IM_BANK38
54	GND	154	GND
55	IM_BANK39	155	IM_BANK39
56	IM_BANK40	156	IM_BANK40
57	IM_BANK41	157	IM_BANK41
58	GND	158	GND
59	IM_BANK42	159	IM_BANK42
60	IM_BANK43	160	IM_BANK43
61	IM_BANK44	161	IM_BANK44
62	GND	162	GND
63	IM_BANK45	163	IM_BANK45
64	IM_BANK46	164	IM_BANK46
65	IM_BANK47	165	IM_BANK47
66	GND	166	GND
67	IM_BANK48	167	IM_BANK48
68	IM_BANK49	168	IM_BANK49
69	IM_BANK50	169	IM_BANK50
70	GND	170	GND
71	IM_BANK51	171	IM_BANK51
72	IM_BANK52	172	IM_BANK52
73	IM_BANK53	173	IM_BANK53
74	GND	174	GND
75	IM_BANK54	175	IM_BANK54
76	IM_BANK55	176	IM_BANK55
77	IM_BANK56	177	IM_BANK56
78	GND	178	GND
79	IM_BANK57	179	IM_BANK57
80	IM_BANK58	180	IM_BANK58
81	IM_BANK59	181	IM_BANK59
82	GND	182	GND
83	IM_BANK60	183	IM_BANK60
84	IM_BANK61	184	IM_BANK61
85	GND	185	GND
86	GND	186	GND
87	GND	187	GND
88	GND	188	GND
89	GND	189	GND
90	GND	190	GND
91	GND	191	GND
92	GND	192	GND
93	IM_CLK	193	IM_CLK
94	GND	194	GND
95	GND	195	GND
96	GND	196	GND
97	GND	197	GND
98	GND	198	GND
99	GND	199	GND
100	GND	200	GND

DO NOT FIT

Interface Module plug (top)

1	GND	101	GND
2	GND	102	GND
3	IM_BANK0	103	IM_BANK0
4	IM_BANK1	104	IM_BANK1
5	IM_BANK2	105	IM_BANK2
6	GND	106	GND
7	IM_BANK3	107	IM_BANK3
8	IM_BANK4	108	IM_BANK4
9	IM_BANK5	109	IM_BANK5
10	GND	110	GND
11	IM_BANK6	111	IM_BANK6
12	IM_BANK7	112	IM_BANK7
13	IM_BANK8	113	IM_BANK8
14	GND	114	GND
15	IM_BANK9	115	IM_BANK9
16	IM_BANK10	116	IM_BANK10
17	IM_BANK11	117	IM_BANK11
18	GND	118	GND
19	IM_BANK12	119	IM_BANK12
20	IM_BANK13	120	IM_BANK13
21	IM_BANK14	121	IM_BANK14
22	GND	122	GND
23	IM_BANK15	123	IM_BANK15
24	IM_BANK16	124	IM_BANK16
25	IM_BANK17	125	IM_BANK17
26	GND	126	GND
27	IM_BANK18	127	IM_BANK18
28	IM_BANK19	128	IM_BANK19
29	IM_BANK20	129	IM_BANK20
30	GND	130	GND
31	IM_BANK21	131	IM_BANK21
32	IM_BANK22	132	IM_BANK22
33	IM_BANK23	133	IM_BANK23
34	GND	134	GND
35	IM_BANK24	135	IM_BANK24
36	IM_BANK25	136	IM_BANK25
37	IM_BANK26	137	IM_BANK26
38	GND	138	GND
39	IM_BANK27	139	IM_BANK27
40	IM_BANK28	140	IM_BANK28
41	IM_BANK29	141	IM_BANK29
42	GND	142	GND
43	IM_BANK30	143	IM_BANK30
44	IM_BANK31	144	IM_BANK31
45	IM_BANK32	145	IM_BANK32
46	GND	146	GND
47	IM_BANK33	147	IM_BANK33
48	IM_BANK34	148	IM_BANK34
49	IM_BANK35	149	IM_BANK35
50	GND	150	GND
51	IM_BANK36	151	IM_BANK36
52	IM_BANK37	152	IM_BANK37
53	IM_BANK38	153	IM_BANK38
54	GND	154	GND
55	IM_BANK39	155	IM_BANK39
56	IM_BANK40	156	IM_BANK40
57	IM_BANK41	157	IM_BANK41
58	GND	158	GND
59	IM_BANK42	159	IM_BANK42
60	IM_BANK43	160	IM_BANK43
61	IM_BANK44	161	IM_BANK44
62	GND	162	GND
63	IM_BANK45	163	IM_BANK45
64	IM_BANK46	164	IM_BANK46
65	IM_BANK47	165	IM_BANK47
66	GND	166	GND
67	IM_BANK48	167	IM_BANK48
68	IM_BANK49	168	IM_BANK49
69	IM_BANK50	169	IM_BANK50
70	GND	170	GND
71	IM_BANK51	171	IM_BANK51
72	IM_BANK52	172	IM_BANK52
73	IM_BANK53	173	IM_BANK53
74	GND	174	GND
75	IM_BANK54	175	IM_BANK54
76	IM_BANK55	176	IM_BANK55
77	IM_BANK56	177	IM_BANK56
78	GND	178	GND
79	IM_BANK57	179	IM_BANK57
80	IM_BANK58	180	IM_BANK58
81	IM_BANK59	181	IM_BANK59
82	GND	182	GND
83	IM_BANK60	183	IM_BANK60
84	IM_BANK61	184	IM_BANK61
85	GND	185	GND
86	GND	186	GND
87	GND	187	GND
88	GND	188	GND
89	GND	189	GND
90	GND	190	GND
91	GND	191	GND
92	GND	192	GND
93	IM_CLK	193	IM_CLK
94	GND	194	GND
95	GND	195	GND
96	GND	196	GND
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98	GND	198	GND
99	GND	199	GND
100	GND	200	GND

SOLC-150-02-F-Q

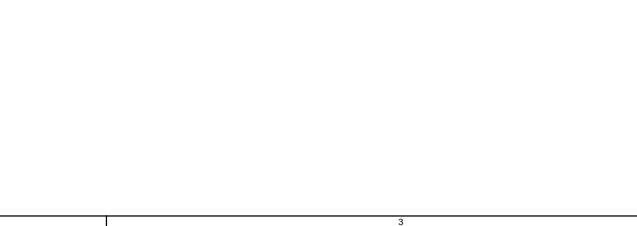
Interface Module plug (top)

1	GND	101	GND
2	GND	102	GND
3	IM_BANK0	103	IM_BANK0
4	IM_BANK1	104	IM_BANK1
5	IM_BANK2	105	IM_BANK2
6	GND	106	GND
7	IM_BANK3	107	IM_BANK3
8	IM_BANK4	108	IM_BANK4
9	IM_BANK5	109	IM_BANK5
10	GND	110	GND
11	IM_BANK6	111	IM_BANK6
12	IM_BANK7	112	IM_BANK7
13	IM_BANK8	113	IM_BANK8
14	GND	114	GND
15	IM_BANK9	115	IM_BANK9
16	IM_BANK10	116	IM_BANK10
17	IM_BANK11	117	IM_BANK11
18	GND	118	GND
19	IM_BANK12	119	IM_BANK12
20	IM_BANK13	120	IM_BANK13
21	IM_BANK14	121	IM_BANK14
22	GND	122	GND
23	IM_BANK15	123	IM_BANK15
24	IM_BANK16	124	IM_BANK16
25	IM_BANK17	125	IM_BANK17
26	GND	126	GND
27	IM_BANK18	127	IM_BANK18
28	IM_BANK19	128	IM_BANK19
29	IM_BANK20	129	IM_BANK20
30	GND	130	GND
31	IM_BANK21	131	IM_BANK21
32	IM_BANK22	132	IM_BANK22
33	IM_BANK23	133	IM_BANK23
34	GND	134	GND
35	IM_BANK24	135	IM_BANK24
36	IM_BANK25	136	IM_BANK25
37	IM_BANK26	137	IM_BANK26
38	GND	138	GND
39	IM_BANK27	139	IM_BANK27
40	IM_BANK28	140	IM_BANK28
41	IM_BANK29	141	IM_BANK29
42	GND	142	GND
43	IM_BANK30	143	IM_BANK30
44	IM_BANK31	144	IM_BANK31
45	IM_BANK32	145	IM_BANK32
46	GND	146	GND
47	IM_BANK33	147	IM_BANK33
48	IM_BANK34	148	IM_BANK34
49	IM_BANK35	149	IM_BANK35
50	GND	150	GND
51	IM_BANK36	151	IM_BANK36
52	IM_BANK37	152	IM_BANK37
53	IM_BANK38	153	IM_BANK38
54	GND	154	GND
55	IM_BANK39	155	IM_BANK39
56	IM_BANK40	156	IM_BANK40
57	IM_BANK41	157	IM_BANK41
58	GND	158	GND
59	IM_BANK42	159	IM_BANK42
60	IM_BANK43	160	IM_BANK43
61	IM_BANK44	161	IM_BANK44
62	GND	162	GND
63	IM_BANK45	163	IM_BANK45
64	IM_BANK46	164	IM_BANK46
65	IM_BANK47	165	IM_BANK47
66	GND	166	GND
67	IM_BANK48	167	IM_BANK48
68	IM_BANK49	168	IM_BANK49
69	IM_BANK50	169	IM_BANK50
70	GND	170	GND
71	IM_BANK51	171	IM_BANK51
72	IM_BANK52	172	IM_BANK52
73	IM_BANK53	173	IM_BANK53
74	GND	174	GND
75	IM_BANK54	175	IM_BANK54
76	IM_BANK55	176	IM_BANK55
77	IM_BANK56	177	IM_BANK56
78	GND	178	GND
79	IM_BANK57	179	IM_BANK57
80	IM_BANK58	180	IM_BANK58
81	IM_BANK59	181	IM_BANK59
82	GND	182	GND
83	IM_BANK60	183	IM_BANK60
84	IM_BANK61	184	IM_BANK61
85	GND	185	GND
86	GND	186	GND
87	GND	187	GND
88	GND	188	GND
89	GND	189	GND
90	GND	190	GND
91	GND	191	GND
92	GND	192	GND
93	IM_CLK	193	IM_CLK
94	GND	194	GND
95	GND	195	GND
96	GND	196	GND
97	GND	197	GND
98	GND	198	GND
99	GND	199	GND
100	GND	200	GND

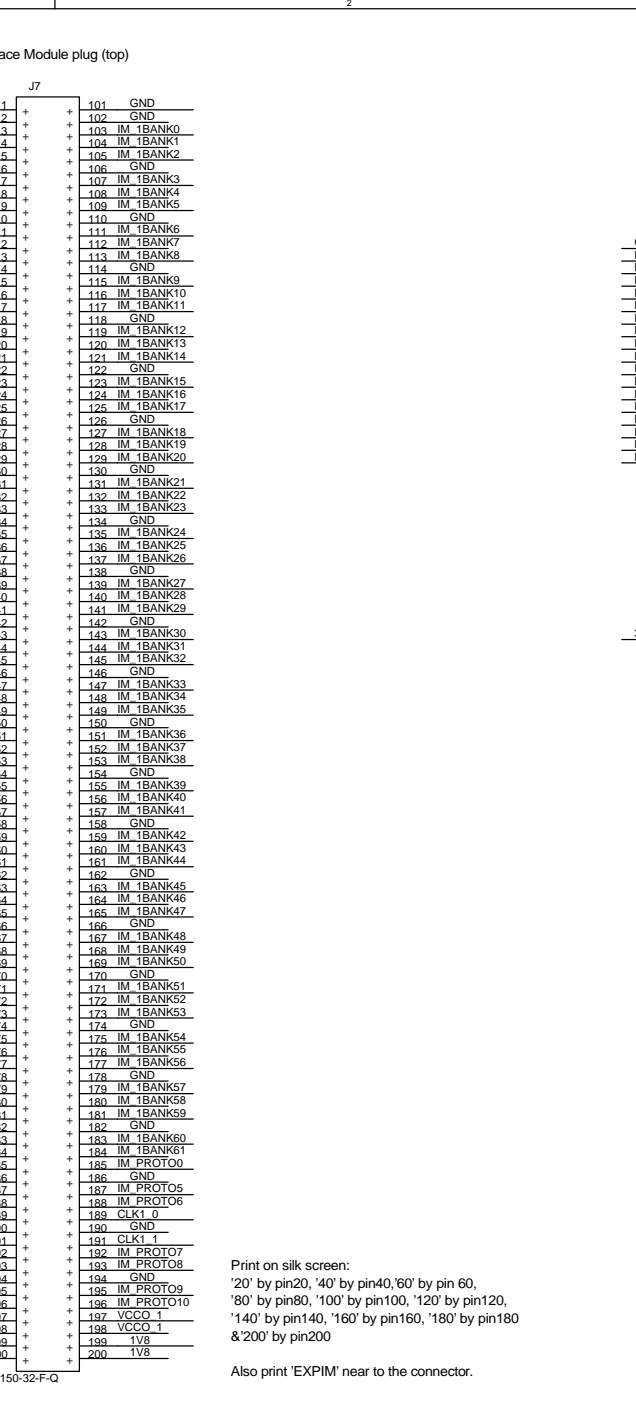
TOLC-150-32-F-Q

DO NOT FIT

LOGIC ANALYSER CONNECTOR

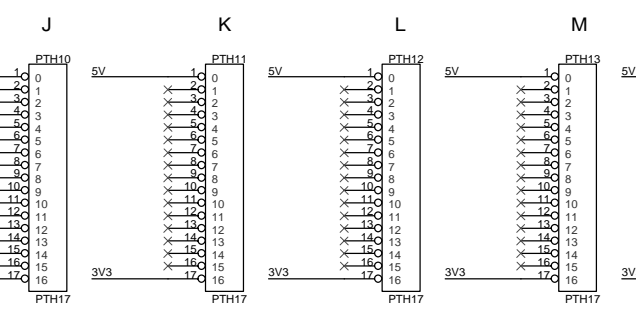


AMP 2-767004-2



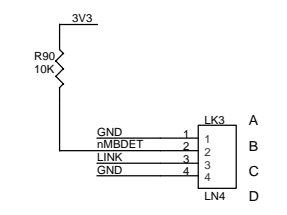
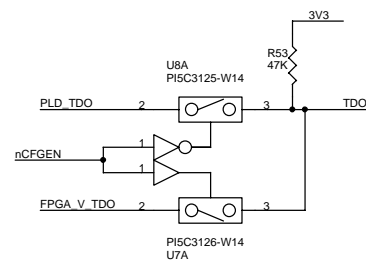
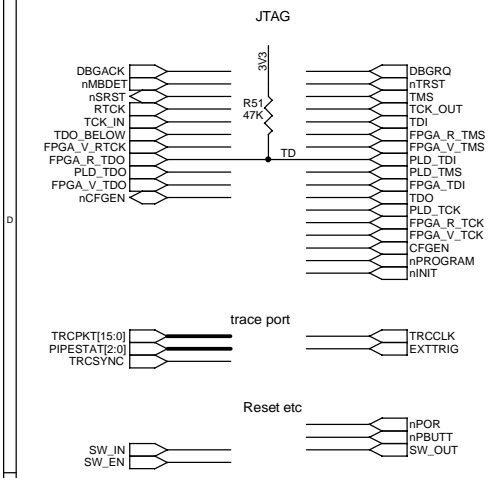
SW DIP-8

PROTOTYPING AREA

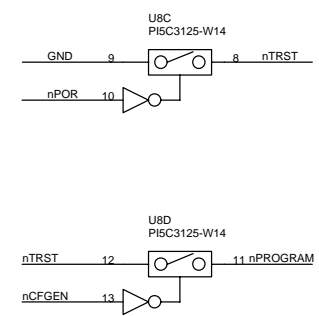
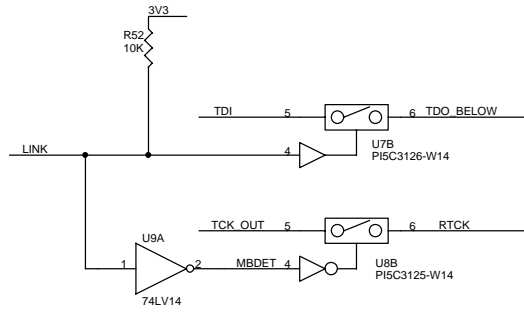


DO NOT FIT

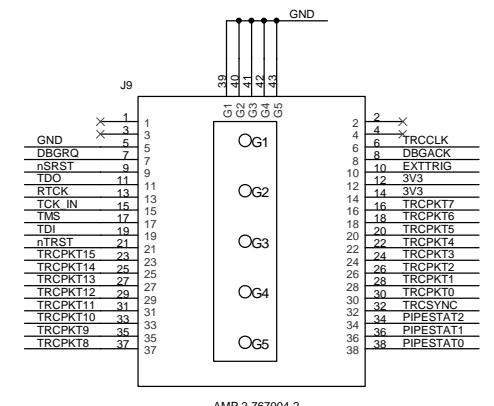
Join TDI to TDO and TCK to RTCK when there is no motherboard
A link can be moved to allow stacking without a motherboard present



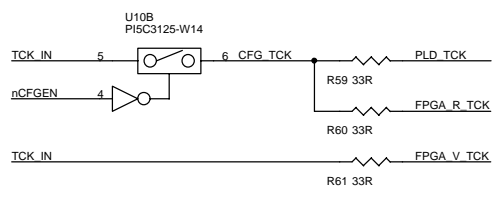
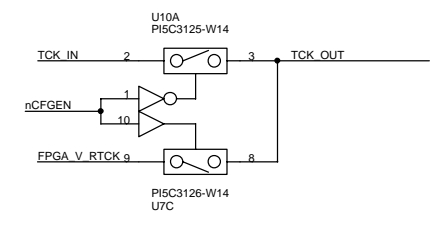
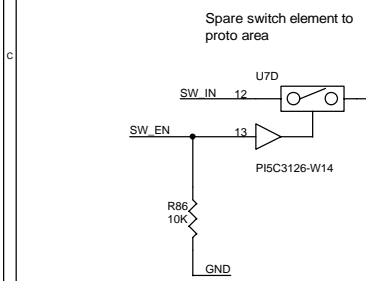
Zero Ohm link positions
 B-C - normal stacking (default)
 A-B - LM at bottom (pos 0) of stack with no motherboard
 C-D - LM at position 1,2 or 3 with no motherboard



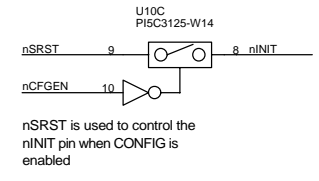
nTRST is used to control the nPROGRAM pin when CONFIG is enabled



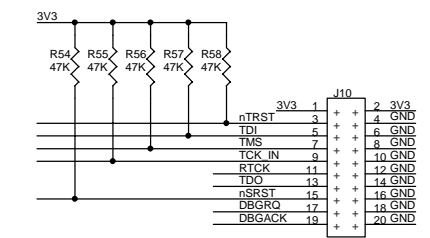
Print on silkscreen Trace - type B pinout



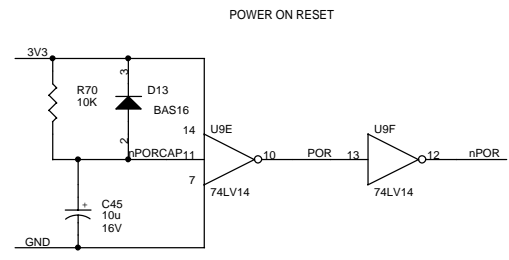
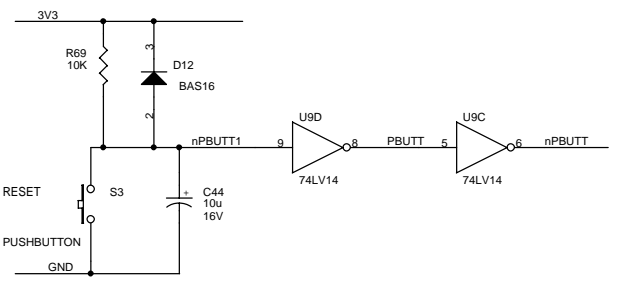
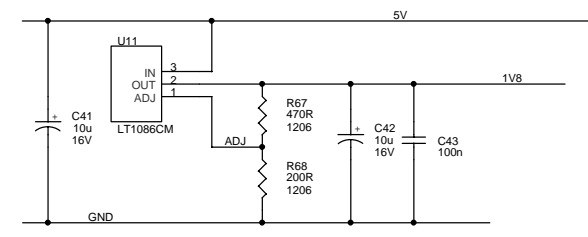
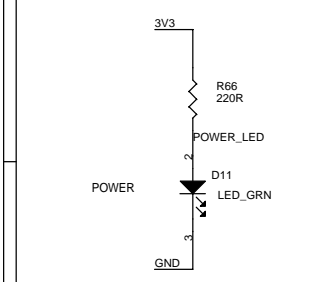
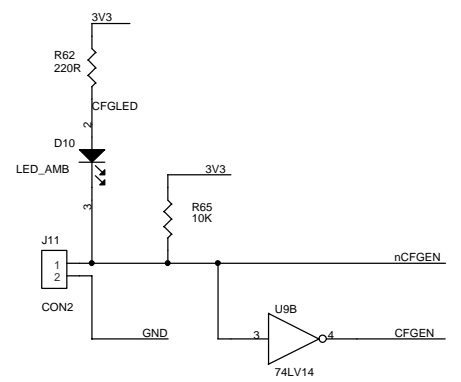
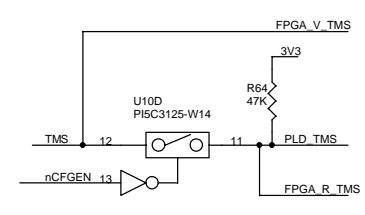
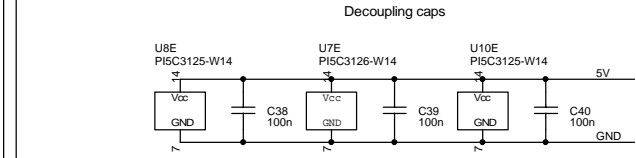
R59 and R60 to be placed close to IC10
 R61 to be placed close to J10



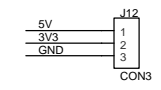
nSRST is used to control the nINIT pin when CONFIG is enabled



Print on silkscreen 'Multi-ICE'



Terminal Block



Mark voltages on the silkscreen (e.g. 'GND', '3V3' and '5V').

